

**AMENDMENTS TO THE CLAIMS:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

1-3 (Cancelled)

4. (Currently Amended) The liquid crystal display apparatus of claim 294, wherein the drain electrode comprises a potential correction site which has a narrowed width.

5-8. (Cancelled)

9. (Currently Amended) The liquid crystal display apparatus of claim 296, wherein the gate electrode comprises a potential correction site which has a narrowed width.

10-11. (Cancelled)

12. (Currently Amended) ~~The liquid crystal display apparatus of claim 11,~~ A liquid crystal display apparatus comprising:

a pair of substrates disposed opposing each other;

a liquid crystal layer sandwiched therebetween;

a data signal transmission line for supplying data signals, laid out on one of the substrates;

a scan signal transmission line for supplying timing signals, laid out on the one of the substrates;

an auxiliary capacitance line for forming an auxiliary capacitance,

a thin-film transistor including a gate electrode which is diverted from the scan signal transmission line, the thin-film transistor being electrically connected to the data and scan signal transmission lines,

a pixel electrode connected to the thin-film transistor;  
an interlayer insulating film generally formed beneath the pixel electrode  
wherein the interlayer insulating film and liquid crystal layer are disposed so as to  
overlie at least part of a diverted portion which is diverted from the auxiliary capacitance  
line;

wherein the pixel electrode is provided with an opening formed in a region on part of the diverted portion of the auxiliary capacitance line.

13. (Previously Amended) The liquid crystal display apparatus of claim 12, wherein the opening of the pixel electrode is formed so as to contact an outer periphery of the pixel electrode.

14. (Currently Amended) ~~The liquid crystal display apparatus of claim 11,~~ A  
liquid crystal display apparatus comprising:

a pair of substrates disposed opposing each other;

a liquid crystal layer sandwiched therebetween;

a data signal transmission line for supplying data signals, laid out on one of the  
substrates;

a scan signal transmission line for supplying timing signals, laid out on the one of  
the substrates;

an auxiliary capacitance line for forming an auxiliary capacitance,

a thin-film transistor including a gate electrode which is diverted from the scan  
signal transmission line, the thin-film transistor being electrically connected to the data  
and scan signal transmission lines,

a pixel electrode connected to the thin-film transistor;

an interlayer insulating film generally formed beneath the pixel electrode

wherein the interlayer insulating film and liquid crystal layer are disposed so as to overlie  
at least part of a diverted portion which is diverted from the auxiliary capacitance line;

wherein the interlayer insulating film is provided with an opening formed in a region thereof on part of the diverted portion of the auxiliary capacitance line.

15-25. (Cancelled)

26. (Currently Amended) The liquid crystal display apparatus of claim 2925, ~~further comprising an auxiliary capacitance line for forming an auxiliary capacitance, and~~ wherein the potential correction site is a narrowed width portion of the branch of the auxiliary capacitance line.

27. (Cancelled)

28. (Previously Presented) A liquid crystal display apparatus comprising:  
a switching device connected to a gate line and a signal line;  
a pixel electrode connected to the switching device;  
an opening formed in the pixel electrode to expose a potential correction site provided therebeneath;  
an interlayer insulation film generally formed beneath the pixel electrode but not beneath the opening which exposes the potential correction site.

29. (Previously Presented) The liquid crystal display apparatus of claim 28, wherein the potential correction site is at one of a drain electrode of the switching device; a gate electrode of the switching device; and a branch of an auxiliary capacitance line.

30. (Previously Presented) The liquid crystal display apparatus of claim 28, wherein the opening is formed at a periphery of the pixel electrode.

31. (Previously Presented) The liquid crystal display apparatus of claim 30, wherein the opening is formed at a periphery of the pixel electrode which is proximate the signal line.

32. (Previously Presented) A liquid crystal display apparatus comprising:

- a switching device connected to a gate line and a signal line;

- a common line;

- a drain electrode connected to the switching device, the drain electrode having a section which overlies at least part of the common line;

- a pixel electrode connected to an electrical connection section of the drain electrode;

- a first correction site provided at a narrowed portion of the drain electrode, the first correction site being situated between the switching device and the electrical connection section of the drain electrode;

- a second correction site comprising a narrowed portion of the drain electrode, the second correction site being intermediate the electrical connection section of the drain electrode and the section of the drain electrode which overlies at least part of the common line;

- an opening formed in the pixel electrode to expose the second potential correction site.

33. (Previously Presented) A liquid crystal display apparatus comprising:

- a switching device connected to a gate line and a signal line, the switching device being situated over the gate line;

- a common line including a common branch diverted from the common line;

- a drain electrode connected to the switching device, the drain electrode having a section which overlies at least part of the common line;

a pixel electrode connected to an electrical connection section of the drain electrode, the pixel electrode also overlying at least a portion of the common branch diverted from the common line;

a first correction site provided at a narrowed portion of the drain electrode, the first correction site being situated between the switching device and the electrical connection section of the drain electrode;

a second correction site comprising a narrowed portion of the drain electrode, the second correction site being intermediate the electrical connection section of the drain electrode and the section of the drain electrode which overlies at least part of the common line;

a first opening formed in the pixel electrode to expose the first potential correction site;

a second opening formed in the pixel electrode to expose the second potential correction site;

a third opening formed in the pixel electrode to expose at least a portion of the common branch diverted from the common line.

34. (Previously Presented) A liquid crystal display apparatus comprising:

a switching device connected to a first gate line and a signal line;

a drain electrode connected to the switching device, the drain electrode having a section which overlies at least part of a second gate line;

a pixel electrode connected to an electrical connection section of the drain electrode;

a first correction site provided at a narrowed portion of the drain electrode, the first correction site being situated between the switching device and the electrical connection section of the drain electrode;

a second correction site comprising a narrowed portion of the drain electrode, the second correction site being intermediate the electrical connection section of the drain

electrode and the section of the drain electrode which overlies at least part of the second gate line;

a first opening formed in the pixel electrode to expose the first potential correction site; and

a second opening formed in the pixel electrode to expose the second potential correction site.

35. (Previously Presented) The apparatus of claim 34, wherein at least one of the first opening and the second opening is formed at a periphery of the pixel electrode.

36. (Previously Presented) The apparatus of claim 34, wherein the narrowed portion comprising the first correction site extends parallel to the first gate line.